

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Tadayoshi Ueda

Application No.: Not Yet Assigned

Filed: Concurrently Herewith Art Unit: N/A

For: POWER SUPPLY SYSTEM AND METHOD
FOR SUPPLYING POWER TO CPU
PROVIDING POWER SAVING MODE

CLAIM FOR PRIORITY

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

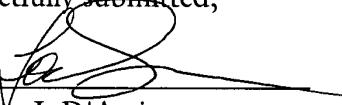
Applicant hereby claims priority under 35 U.S.C. 119 based on the following prior foreign application filed in the following foreign country on the date indicated:

Country	Application No.	Date
Japan	2002-289862	October 2, 2002

In support of this claim, a certified copy of the said original foreign application will be filed shortly.

Dated: October 1, 2003

Respectfully submitted,

By 
Thomas J. D'Amico

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